

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of: **Akira MATSUDA et al.**

Group Art Unit: **1794**

Application Number: **10/719,020**

Examiner: **Cathy Fong Fong Lam**

Filed: **November 24, 2003**

Confirmation Number: **9168**

For: **METHOD FOR PREPARING A CIRCUIT BOARD MATERIAL  
HAVING A CONDUCTIVE BASE AND A RESISTANCE LAYER**

Attorney Docket Number: **032130**  
Customer Number: **38834**

**AMENDMENT UNDER 37 C.F.R. §1.111**

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

March 1, 2010

Sir:

This paper is filed in response to the Office Action dated September 29, 2009, the response due date extended to February 28, 2010 by a two-months Extension of Time.

**Amendments to the Claims** begin on page 2 of this paper.

**Remarks** begin on page 5 of this paper.